

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

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|--|---|-----------------------|
| <b>Title of Invention</b>  | SELF-ALIGNED DRAIN/CHANNEL JUNCTION IN VERTICAL PASS TRANSISTOR DRAM CELL DESIGN FOR DEVICE SCALING |                       |
| <p>Application Number :</p> <p>Date :</p> <p>First Named Applicant: Geng Wang</p> <p>Confirmation Number:</p> <p>Attorney Docket Number: FIS920030209US1</p>   |   |                       |
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| <b>Submitted By:</b>   | <b>Elec. Sign.</b>  | <b>Sign. Capacity</b> |
| H. Daniel Schnurmann<br>Registered Number: 35791   | H. Daniel Schnurmann  | Agent                 |

| <b>Documents being submitted:</b> | <b>Files</b>   |
|-----------------------------------|--|
| us-fee-sheet                      | FIS920030209US1-usfees.xml<br>us-fee-sheet.xsl<br>us-fee-sheet.dtd   |
| us-ids                            | FIS920030209US1-usidst.xml<br>us-ids.dtd<br>us-ids.xsl   |
| us-request                        | FIS920030209US1-usrequ.xml<br>us-request.dtd<br>us-request.xsl   |
| us-declaration                    | fis920030209us1declaration1.tif  |
| us-declaration                    | fis920030209us1declaration2.tif  |
| us-declaration                    | fis920030209us1declaration3.tif  |
| application-body                  | FIS920030209US1-trans.xml<br>us-application-body.xsl<br>application-body.dtd<br>wipo.ent<br>mathml2.dtd<br>mathml2-qname-1.mod<br>isoamsa.ent<br>isoamsb.ent<br>isoamsc.ent<br>isoamsn.ent<br>isoamso.ent<br>isoamsr.ent<br>isogrk3.ent<br>isomfrk.ent<br>isomopf.ent<br>isomscr.ent<br>isotech.ent<br>isobox.ent<br>isocyr1.ent<br>isocyr2.ent<br>isodia.ent<br>isolat1.ent<br>isolat2.ent<br>isonum.ent<br>isopub.ent<br>mmlextra.ent<br>mmlalias.ent<br>soextblk.dtd<br>FIS920030209US1Drawing1.tif<br>FIS920030209US1Drawing2.tif<br>FIS920030209US1Drawing3.tif<br>FIS920030209US1Drawing4.tif<br>FIS920030209US1Drawing5.tif<br>FIS920030209US1Drawing6.tif<br>FIS920030209US1Drawing7.tif<br>FIS920030209US1Drawing8.tif<br>FIS920030209US1Drawing9.tif |

**Comments**

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